

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L13	0	(self adj test adj controller with self adj test adj instruction near2 method\$5 same memory near2 test\$3).clm.	US-PGPUB	OR	ON	2007/04/05 12:29

Interference Search History

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L13	0	(self adj test adj controller with self adj test adj instruction near2 method\$5 same memory near2 test\$3).clm.	US-PGPUB	OR	ON	2007/04/05 12:29
L12	22	("20020138800" "20030074619" "5640509" "5668816" "5712822" "5912901" "5923599" "6065134" "6324093" "6505308" "6523135").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/05 12:26
L11	1	10/074506	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/05 11:42
L10	2	"6658611".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/05 11:34
L9	4	("5224101" "5301156").pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/05 10:16
L8	324	(( (configur\$4 program\$5 instruction (op micro) adj (code instruction))) near3 (self-test self adj test built-in adj self-test \$1BIST) near2 (controller scheduler register engine circuit apparatus device unit)) same (memory \$1RAM))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/05 10:16
L6	324	(( (configur\$4 program\$5 instruction (op micro) adj code)) near3 (self-test self adj test built-in adj self-test \$1BIST) near2 (controller scheduler register engine circuit apparatus device unit)) same (memory \$1RAM))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/05 09:49
L7	183	L2 and L6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/05 09:47

## EAST Search History

L5	401	L2 and L4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/05 09:46
L4	1196	(((self-test self adj test built-in adj self-test \$1BIST) with (configur\$4 program\$5 instruction (op micro) adj code)) with (controller scheduler register engine circuit apparatus device unit)) same (memory \$1RAM))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/05 09:44
L3	1199	L2 and L1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/05 09:36
L1	20452	(((configur\$4 program\$5 instruction (op micro) adj code)) near5 (self-test self adj test built-in adj self-test \$1BIST array) near6 (controller scheduler register engine circuit apparatus device unit)) same (memory \$1RAM))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/05 09:34
S6	28	(((configur\$4 program\$5) near5 (self-test self adj test built-in adj self-test built-in adj self adj test \$1BIST array adj built-in adj self-test ABIST PBIST) near6 (controller scheduler register engine circuit apparatus device unit function))) and (logical near4 address))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/05 09:21
L2	15917	(714/701,702,718-720,725,733 365/200,201,230.06 438/11).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/05 09:21
S20	18	((((configur\$4 program\$5) near5 (self-test self adj test built-in adj self-test \$1BIST array with built-in adj self-test ABIST PBIST) near6 (controller scheduler register engine circuit apparatus device unit)) same (memory \$1RAM))) and (logical near4 address))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/05 09:17

## EAST Search History

S21	11	S18 and S20	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/23 16:42
S19	6508	S18 and "24"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/23 16:41
S18	13655	(714/701,702,718-720,725,733 365/200,201,230.06 438/11).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/23 16:41
S17	3	"472818-\$".did.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/07 11:16
S16	2	"20030167428-\$".did.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/07 11:15
S15	0	"US-20030167428-\$".did.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/05 17:33
S14	2	"6001662".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/05 17:14
S13	24	(((((physical near3 address near4 (map\$4 or generat\$3)) same (logical near3 address)) and memory)) and (self-test self adj test built-in adj self-test built-in adj self adj test \$1BIST array adj built-in adj self-test ABIST PBIST) near6 (controller scheduler register engine circuit apparatus device unit function))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/04 16:39

## EAST Search History

S5	678	((configur\$4 program\$5) near5 (self-test self adj test built-in adj self-test built-in adj self adj test \$1BIST array adj built-in adj self-test ABIST PBIST) near6 (controller scheduler register engine circuit apparatus device unit function))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/04 16:31
S12	1188	((physical near3 address near4 (map\$4 or generat\$3)) same (logical near3 address)) and memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/04 16:28
S11	1188	((physical near3 address near4 (map\$4 or generat\$3)) same (logical near3 address)) and memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/04 16:28
S10	374	((physical near3 address near map\$4) same (logical near3 address)) and memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/04 16:25
S8	2	"5633877".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/04 16:23
S9	1	"5173906".PN.	US-PGPUB; USPAT	OR	ON	2004/06/04 15:58
S7	0	"5633877""pn"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/04 15:57
S4	16	(((((configur\$4 program\$5) near5 (self-test self adj test built-in adj self-test \$1BIST array with built-in adj self-test ABIST PBIST) near6 (controller scheduler register engine circuit apparatus device unit)) same (memory \$1RAM))) and (logical near4 address))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/04 15:29
S1	329	((configur\$4 program\$5) near5 (self-test self adj test built-in adj self-test \$1BIST array with built-in adj self-test ABIST PBIST) near6 (controller scheduler register engine circuit apparatus device unit)) same (memory \$1RAM))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/04 15:25

## EAST Search History

S3	2	((((configur\$4 program\$5) near5 (self-test self adj test built-in adj self-test \$1BIST array with built-in adj self-test ABIST PBIST) near6 (controller scheduler register engine circuit apparatus device unit)) same (memory \$1RAM))) and ((map\$4 or convert\$3) same (physical coordinate "XY") same (logical or binary or bits)) same (row same column))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/04 15:19
S2	0	((((configur\$4 program\$5) near5 (self-test self adj test built-in adj self-test \$1BIST array with built-in adj self-test ABIST PBIST) near6 (controller scheduler register engine circuit apparatus device unit)) same (memory \$1RAM))) and ((map\$4 or convert\$3) near4 (physical coordinate "XY") near6 (logical or binary or bits)) same (row same column))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/04 15:17